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Docket No. YOR920030175US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Allen et al.
Case: YOR920030175US1
Serial No.: 10/661,041
Filing Date: September 12, 2003
Group: 2811
Examiner: Cuong Q. Nguyen

I hereby certify that this paper is being deposited on this date with the
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Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Susan Futura Date: November 1, 2005

Title: Techniques for Patterning Features in Semiconductor Devices

RESPONSE AFTER FINAL REJECTION

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Commissioner for Patents
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Sir:

In response to the outstanding final Office Action dated July 1, 2005, Applicants submit the following remarks.